

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/762,781 ZHANG ET AL.	
		Examiner	Art Unit	Page 1 of 1 Brandon W. Bowers 2825

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,617,325 A	04-1997	Schaefer, Thomas J.	716/6
*	B	US-6,367,061 B1	04-2002	Kurokawa et al.	716/10
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kim et al. "A timing-driven data path layout synthesis with integer programming", International Conference on Computer Aided Design, Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design, San Jose, California, United States, Pages: 716 - 719, 1995
	V	Long, "Logic Design Principles and Examples", VLSI Handbook, Ch. 71, CRC/IEEE Press, 2000
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.